

STUDY MODULE DESCRIPTION FORM		
Name of the module/subject Electronics and Power Electronics		Code 1010321331010323752
Field of study Electrical Engineering	Profile of study (general academic, practical) (brak)	Year /Semester 2 / 3
Elective path/specialty -	Subject offered in: Polish	Course (compulsory, elective) obligatory
Cycle of study: First-cycle studies	Form of study (full-time, part-time) full-time	
No. of hours Lecture: 30 Classes: - Laboratory: - Project/seminars: -		No. of credits 2
Status of the course in the study program (Basic, major, other) (brak)		(university-wide, from another field) (brak)
Education areas and fields of science and art technical sciences Technical sciences		ECTS distribution (number and %) 2 100% 2 100%
Responsible for subject / lecturer: dr hab. inż. Michał Gwóźdź email: michal.gwozdz@put.poznan.pl tel. 61 665 2646 Faculty of Electrical Engineering ul. Piotrowo 3A 60-965 Poznań		
Prerequisites in terms of knowledge, skills and social competencies:		
1	Knowledge	Basic knowledge of physics, electrical engineering, and mathematical analysis
2	Skills	Analysis and synthesis of circuits, carrying out activities in the field of basic account of operator. The ability to effectively self-education in a field related to the chosen field of study
3	Social competencies	It is aware of the need to broaden their competence, willingness to cooperate within the team
Assumptions and objectives of the course: Getting to know the construction, parameters and applications of basic electronic components. Getting to know the principles of operation of analog and digital electronic circuits. Skills in electronic design at the basic level.		
Study outcomes and reference to the educational results for a field of study		
Knowledge: 1. Can describe the operating principles and parameters of basic electronic components, characterize the structure and use of basic analog and digital electronic circuits - [K_W04 + K_W07 + K_W14 +++] 2. Can describe the basic criteria for the design of electronic circuits - [K_W04 + K_W14 +++]		
Skills: 1. Knows how to apply the knowledge in electronics to analyze the operation of basic analog and digital electronic circuits - [K_U01 + K_U03 ++] 2. Can define the criteria necessary for the proper design of electronic system-level - [K_U01 ++ K_U03 +]		
Social competencies: 1. Is able to think and act in an entrepreneurial way in the area of electronic design - [K_K02 ++]		
Assessment methods of study outcomes		
Assessment of the knowledge and skills shown on the written test		
Course description		

The properties and characteristics of the basic elements and electronic devices: passive components, p-n junction, semiconductor diodes, bipolar transistors and field systems and their operation and application. Semiconductor optoelectronic devices? properties and application examples. The feedback in analog circuits. Operational amplifiers - the ideal and the real, properties, parameters, applications. Power amplifiers? division, properties and applications. Generators electronics: vibration generation conditions, types and application generators. Analog filters: linear systems, types, projects and the use of filters. Rectifier and power systems. Basics of digital technology: the binary system of writing numbers, logic states and logical operations? introduction of (elements of logic, logic, truth table, Karnaugh table), digital combinational circuits and sequential. The use of digital circuits. TTL. Semiconductor memory: general classification, discussion of the basic properties of certain types of memory.

Basic bibliography:

1. W. Golde, Układy elektroniczne, Wydanie drugie, WNT, Warszawa, 1974
2. Z. Kulka, M. Nadachowski, Analogowe układy scalone, WKŁ, W-wa 1980
3. Z. Kulka, M. Nadachowski, Wzmacniacze operacyjne i ich zastosowania cz.1 i 2, WNT, W-wa 1982
4. P. Horowitz, W. Hill, Sztuka elektroniki, t. I / II, WKŁ, 1997
5. J. Kalisz, Podstawy techniki cyfrowej, WKiŁ, Warszawa 1998
6. P. Górecki, Wzmacniacze operacyjne, BTC, Warszawa 2002

Additional bibliography:

1. U. Tietze, Ch. Schenk, Układy półprzewodnikowe, WNT, 1996
2. M. P. Kaźmierkowski, J. T. Matysik, Wprowadzenie do elektroniki i energoelektroniki, Oficyna Wyd. PW, Warszawa 2005

Result of average student's workload

Activity	Time (working hours)
1. Participation in lecture classes	30
2. Participation in consultations	5
3. Przygotowanie do egzaminu	15

Student's workload

Source of workload	hours	ECTS
Total workload	50	2
Contact hours	35	2
Practical activities	0	0